

IDS 08/29/03

PTO/SB/08A (10-01)

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Substitute for form 1449A/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(use as many sheets as necessary)</i>			<b>Complete if Known</b>		
			Application Number		
			Filing Date		
			First Named Inventor Philip E. MAY		
			Art Unit		
			Examiner Name		
Sheet	1	of	1	Attorney Docket Number CML00770D	

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code <sup>2</sup> (if known)			
2		US-5495817	02-27-1998	Yamada	
		US-5780877	08-04-1998	Nishiyama et al.	
		US-6442701	08-27-2002	Hurd	
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Substitute for form 1449B/PTO		<b>Complete if Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (use as many sheets as necessary)		Application Number	10/652,134
		Filing Date	3/29/2003
		First Named Inventor	May et al.
		Art Unit	2185
		Examiner Name	Daniel H. Pan
		Attorney Docket Number	CML00770D
Sheet	1	of	1

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cita. No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
h		Al-Mouhamed, M., "Lower Bound on the Number of Processors and Time for Scheduling Precedence Graphs with Communication Costs", IEEE, December 1990	
h		Samadzadeh, F. et al., "A Heuristic Multiprocessor Scheduling Algorithm for Creating Near-Optimal Schedules Using Task System Graphs", ACM, 1992	
h		Prasanna, G.N., et al., "Generalized Multiprocessor Scheduling for Directed Acyclic Graphs", IEEE, 1994	
h		Kwok, Y. et al., "Static Scheduling Algorithms for Allocating Directed Task Graphs to Multiprocessors", ACM, December 1999	

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				Application Number	10/652,134
				Filing Date	3/29/2003
				First Named Inventor	May et al.
				Art Unit	2185
				Examiner Name	Daniel H. Pan
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<i>W</i>		3,718,912	Feb-73	Hasbrouck et al.	
<i>W</i>		4,128,880	Dec-78	Cray Jr.	
<i>W</i>		4,744,043	May-88	Kloker	
<i>W</i>		4,760,545	Jul-88	Inagami et al.	
<i>W</i>		4,807,183	Feb-89	Kung et al.	
<i>W</i>		4,825,361	Apr-89	Omoda et al.	
<i>W</i>		4,918,600	Apr-90	Harper et al.	
<i>W</i>		5,206,822	Apr-93	Taylor	
<i>W</i>		5,390,352	Feb-95	Kinoshita	
<i>W</i>		5,450,607	Sep-95	Kowalczyk et al.	
<i>W</i>		5,495,617	Feb-96	Yamada	
<i>W</i>		5,652,909	Jul-97	Kodosky	
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<i>W</i>		5,936,953	Aug-99	Simmons	
<i>W</i>		5,966,528	Oct-99	Wilkinson et al.	
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<i>W</i>		5,999,736	Dec-99	Gupta et al.	
<i>W</i>		6,052,766	Apr-00	Betker et al.	
<i>W</i>		6,064,819	May-00	Franssen et al.	
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				First Named Inventor	May et al.
				Art Unit	2185
				Examiner Name	Daniel H. Pan
Sheet	2	of	2	Attorney Docket Number	CML00770D

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		LAM, M., Software Pipelining: An Effective Scheduling Technique for VLIW Machines, Proceedings of the SIGPLAN '88 Conference on Programming Language Design and Implementation, Atlanta, Georgia, June 22-24, 1988, pp. 318-328	
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		KAVI, K.M. et al., A Formal Definition of Data Flow Graph Models, IEEE Transactions on Computers, Vol C-35, No. 11, Nov 1986, pp. 940-948	
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		WULF, WILLIAM A. "Evaluation of the WM Architecture." Proceedings of the 19 <sup>th</sup> Annual International Symposium on Computer Architecture: 1992, pp. 382-390, ACM 0-89791-509-7/92/0005/0382.	
		TALLA, D., Architectural techniques to Accelerate Multimedia Applications on General-Purpose Processors, Dissertation, August 2001, pp. 94-125	
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